Ref	Libe	Soarch Quant	DBs	Default	Plurals	Time Stamp
#	Hits	Search Query	DBS	Operator	Plurais	Time Stamp
L1	1143119	(semiconductor silicon si gaas) with (substrate board carrier layer structure)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:02
L2	251548	1 with (thru via hole through throughhole opening)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:02
L3	3618	(elongat\$4 annular rectang\$4) with 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 14:32
L4	1829	(sidewall wall core side center middle inside) same 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:03
L5	255628	1 with (thru via hole plug\$5 through throughhole opening)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:27
L6	36952	(elongat\$4 annul\$6 ringlike ring circ\$4 concentric rectang\$4) with 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:02
L7	10495	(sidewall wall core side center middle inside) same 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:28
L8	4784	7 and (cte match\$4 coefficient thermal expansion)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 14:39

L9	580	(elastic\$4) and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:28
L10	580	(elastic\$4) and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 16:42
L11	580	(elastic\$4) and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 17:28
L12	580	(elastic\$4) and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 17:59
L13	580	(elastic\$4) and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:04
L14	49836	silicate with glass	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:25
L15	2503	14 with (thru via hole through throughhole opening)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:26
L16	1452	15 and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:02

L17	1078	(sidewall wall core side center middle inside) and 16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:13
L18	19	(elastic\$4) and 17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:04
L19	1059	17 not 18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:14
L20	1059	19 not 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:14
L21	75049	PSG BPSG 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:25
L22	9178	21 with (thru via hole through throughhole opening)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:26
L23	4500	1 same 22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:26
L24	13001736	23with (thru via hole plug\$5 through throughhole opening)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:27

L25	4500	23 same (thru via hole plug\$5 through throughhole opening)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:27
L26	1062	(sidewall wall core side center middle inside) same 25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:28
L27	7	(elastic\$4) and 26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/14 18:28